

# Profir-Petru Pârțachi

E-Mail: me@partachi.com

<b>Education</b>	<b>PhD in Computer Science</b> <i>Centre for Research on Evolution, Search and Testing</i> <b>PhD Thesis:</b> 'Improving Software Project Health Using Machine Learning' Supervised by <i>Prof. Earl T. Barr (e.barr@ucl.ac.uk)</i>	<b>September 2016 – December 2020</b> <i>University College London, Gower Street, London WC1E 6BT</i>
	<b>Computer Science Tripos</b> <i>King's College, University of Cambridge</i> BA in Computer Science <b>BA Thesis:</b> 'Deck building in Hearthstone Using a Genetic Algorithm.'	<b>September 2013 – July 2016</b> <i>Cambridge, United Kingdom</i>
<b>Work Experience</b>	<b>Post-doctoral Researcher</b> <i>National Institute of Informatics</i> Research into the naturalness properties of structured representations of source-code. Supervised by Assoc. Prof. Mahito Sugiyama	<b>April 2022 – now</b> <i>Chiyoda-ku, Tokyo, Japan</i>
	<b>Freelance Researcher</b> <i>National Institute of Informatics</i> Research into the naturalness properties of structured representations of source-code. Consulting for Assoc. Prof. Mahito Sugiyama	<b>April 2021 – April 2022</b> <i>Chișinău, Republic of Moldova</i>
	<b>Research Internship</b> <i>National Institute of Informatics</i> Worked on the efficient processing of spatiotemporal data for anomaly detection using Graph Kernels. Supervised by Assoc. Prof. Mahito Sugiyama	<b>October 2018 – April 2019</b> <i>Chiyoda-ku, Tokyo, Japan</i>
	<b>Hardware/Software Engineer Intern</b> <i>Computer Laboratory, University of Cambridge</i> Worked within the lowRISC team to provide: <ul style="list-style-type: none"><li>• Hardware implementations of DCT, IDCT and colour space conversions for MPEG2 as AXI-stream accelerators.</li><li>• Hardware logic to interface AXI-stream accelerators with the lowRISC CPU chip.</li></ul>	<b>June 2016 – September 2016</b> <i>Cambridge, United Kingdom</i>
	<b>Software Developer Intern</b> <i>Amazon Instant Video</i> Worked on providing an auditing infrastructure by: <ul style="list-style-type: none"><li>• Writing a plug-in to wrap calls to backend systems to log calls and responses.</li><li>• Storing intercepted calls and pre-processing stored data in Amazon Redshift for auditing reports.</li></ul>	<b>June 2015 – October 2015</b> <i>London, United Kingdom</i>
<b>Publications</b>	<p>[1] Pârțachi, P.-P., White, D. R., &amp; Barr, E. T., <b>Aide-mémoire: Improving a Project's Collective Memory via Pull Request-Issue Links</b>. In <i>ACM Transactions on Software Engineering and Methodology</i>, ACM., May, 2022. <a href="https://github.com/PPPI/a-m">https://github.com/PPPI/a-m</a></p> <p>[2] Pârțachi, P.-P., Dash, S. K., Allamanis, M., &amp; Barr, E. T., <b>Flexeme: Untangling Commits Using Lexical Flows</b>. In <i>28th ACM Joint European Software Engineering Conference and Symposium on the Foundations of Software Engineering, (ESEC/FSE 2020)</i>. Sacramento, California, United States; ACM, November, 2020; <a href="https://partachi.com/Flexeme">https://partachi.com/Flexeme</a></p> <p>[3] Pârțachi, P.-P., Treude, C., Dash, S. K., &amp; Barr, E. T., <b>POSIT: Simultaneously Tagging Natural and Programming Languages</b>. In <i>42nd International Conference</i></p>	

on Software Engineering (ICSE '20).Seoul, Republic of Korea; ACM., July 2020;  
<https://partachi.com/POSIT>

[4] Pârțachi, P.-P.. **Improving Software Project Health Using Machine Learning**. PhD diss., UCL (University College London), 2020.

<b>Teaching Experience</b>	<b>COMPM203 Verification and Validation</b> <i>Teaching Assistant</i> Leading problem based workshops, assisting exam setting, and exam marking	<b>January 2020 – July 2020</b> <i>University College London</i>
	<b>COMP103P Applied Software Development</b> <i>Teaching Assistant</i> Laboratory Supervisor and Group Project Supervisor	<b>January 2018 – April 2018</b> <i>University College London</i>
	<b>COMPM203 Verification and Validation</b> <i>Teaching Assistant</i> Coursework writing and marking	<b>January 2018 – April 2018</b> <i>University College London</i>
	<b>COMP213P Systems Engineering</b> <i>Teaching Assistant</i> Group Project Supervisor	<b>October 2017 – April 2018</b> <i>University College London</i>

<b>Awards</b>	<b>Cambridge Commonwealth Trust 2013-2014</b> <i>For the purpose of BA Computer Science Tripos at King's College, Cambridge</i>
	<b>HMC Reduced Fee Scheme 2012</b> <i>For the purpose of attending Seaford College for UK A-levels</i>

<b>Reviews</b>	<b>Conferences</b> <ul style="list-style-type: none"><li>– Program Committee member for the Research Track at <b>SANER 2023</b></li><li>– Sub-reviewing for <b>ASE 2022</b></li><li>– Program Committee member for the Research Track at <b>SANER 2022</b></li><li>– Sub-reviewing for <b>ISSTA 2021</b></li><li>– Program Committee member for the Mining Challenge at <b>MSR 2021</b></li><li>– Sub-reviewing for <b>SANER 2021</b></li><li>– Sub-reviewing for <b>ICSE 2021</b></li><li>– Sub-reviewing Registered Studies for <b>ICSME 2020</b></li><li>– Sub-reviewing for <b>ASE 2020</b></li><li>– Sub-reviewing for <b>MSR 2020</b></li><li>– Sub-reviewing for <b>FSE 2019</b></li><li>– Sub-reviewing for <b>ISSTA 2019</b></li><li>– Sub-reviewing for <b>ASE 2018</b></li><li>– Sub-reviewing for <b>ECOOP 2018</b></li><li>– Sub-reviewing for <b>ISSTA 2018</b></li><li>– Sub-reviewing for <b>MSR 2017</b></li></ul>
	<b>Journals</b> <ul style="list-style-type: none"><li>– Reviewing for <b>TOSEM 2022</b></li><li>– Reviewing for <b>JSS 2022</b></li></ul>

- Reviewing for **JSS 2021**
- Reviewing for **EMSE 2021**
- Reviewing for **MTAP 2020**
- Sub-reviewing for **EAAI 2020**
- Sub-reviewing for **TSE 2017**

**Technology Skills**

**Programming Languages:** Python, Java, Haskell, SystemVerilog.  
**Theorem Proof Assistants:** Coq.

**Language Skills**

**Native:** Romanian.  
**Fluent:** English, Russian.  
**Intermediate:** Czech, German.  
**Beginner:** Japanese.